

LCM Specification

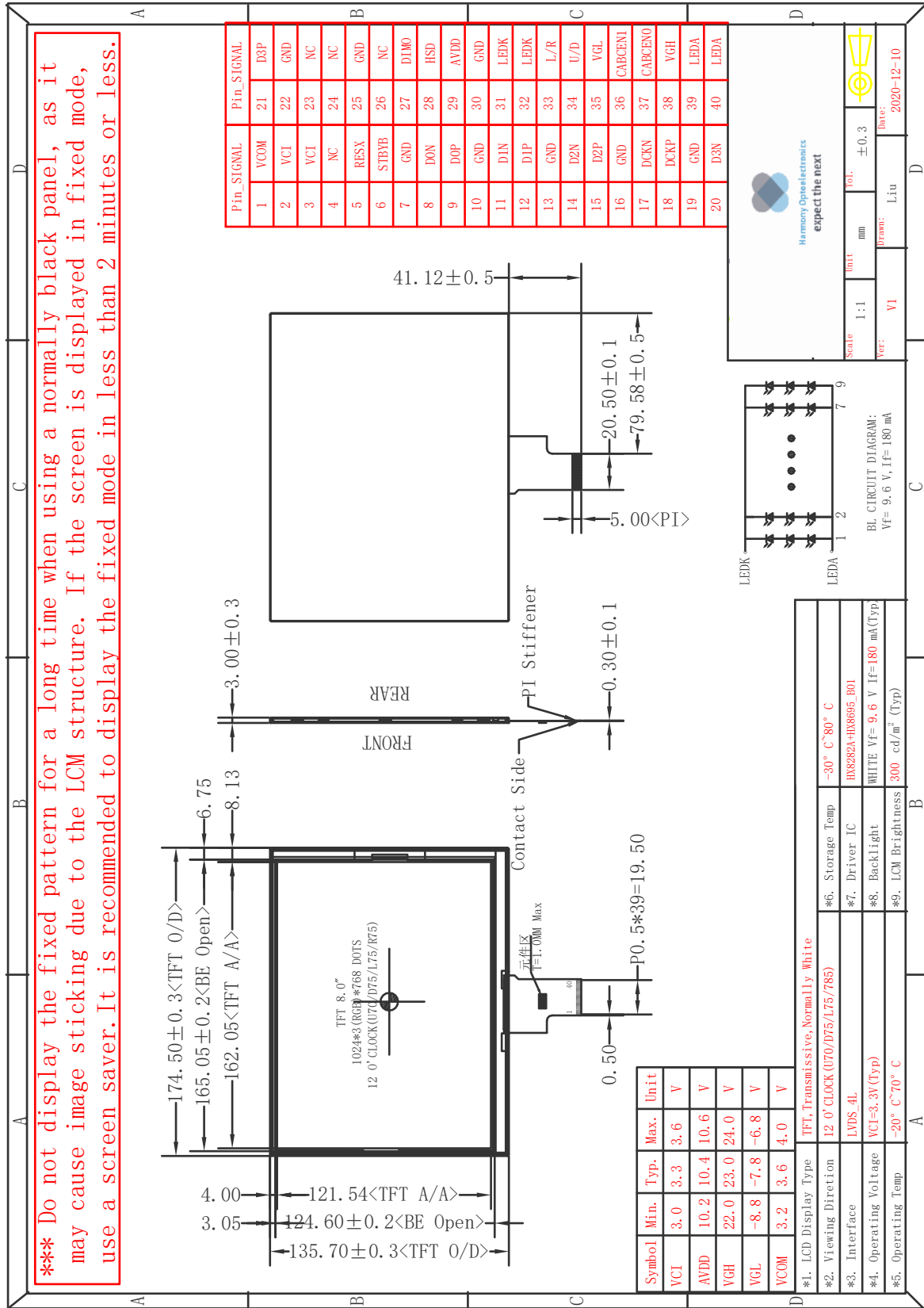
Product type
产品内容

TFT LCD Module
1024 x 3RGB x 768 Dots
8.0”TFT LCD

1. General Feature:

Item	Standard Value	Unit
Display Size	8.0"	--
Number of Pixels	1024(H)x3(RGB)*768(V)	--
Active Area	162.05(H) * 121.54(V)	mm
Outline Dimension	174.50(H) × 135.70(V) × 3.00(D)	mm
Viewing Direction	12 O'clock	-
Interface	LVDS 4L	-
Panel Driver IC	HX8282A+HX8695_B01	-
Panel Driver Condition	VCI=3.3V	V
Backlight	White LED	-
Touch Panel	Without Touch Panel	-
Cap Touch Driver IC	---	-
Cap Touch Driver Condition	---	V
Operation Temperature	-20~70	°C
Storage Temperature	-30~80	°C

2. Outline Dimensions



3. Pin Description

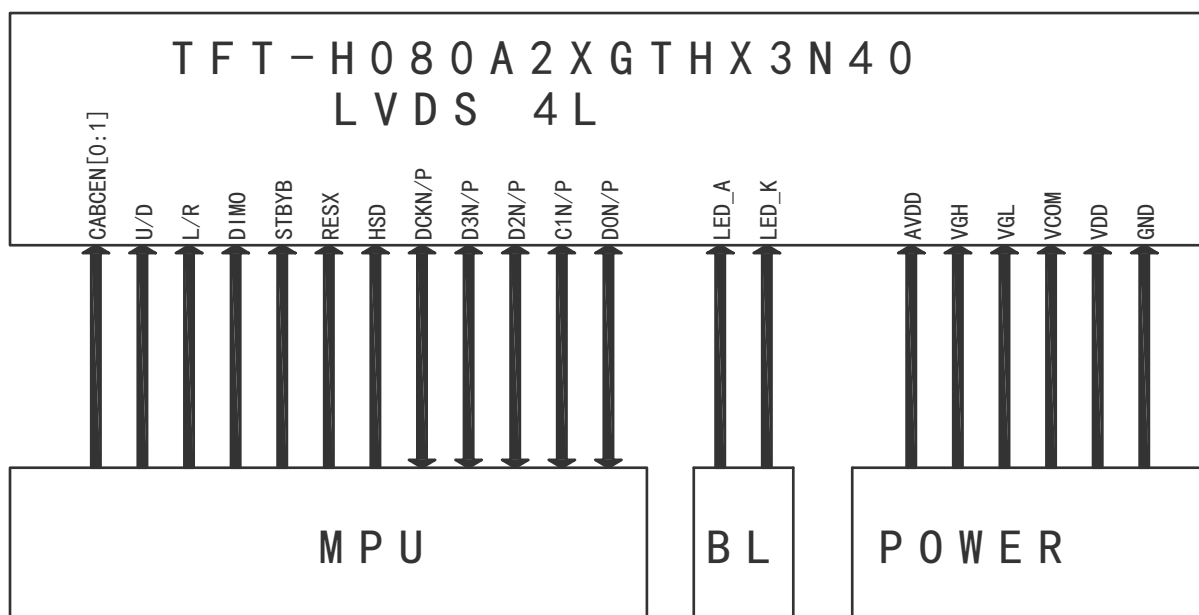
3.1 Pin Description

Pin NO.	Symbol	Description
1	VCOM	TFT Common Electrode Voltage
2,3	VDD	Analog Power
4	NC	Disconnect pin
5	RESX	Global reset pin.Active low to enter Reset State. Suggest to connecting with an RC reset circuit for stability.
6	STBYB	Standby mode control. Normally pull High. When STB=H, all the functions are on. (Default pulls high). When STB=L, TCON and source driver are off and all output are High-z.
7	GND	Ground
8	D0N	Negative LVDS differential data inputs
9	D0P	Positive LVDS differential data inputs
10	GND	Ground
11	D1N	Negative LVDS differential data inputs
12	D1P	Positive LVDS differential data inputs
13	GND	Ground
14	D2N	Negative LVDS differential data inputs
15	D2P	Positive LVDS differential data inputs
16	GND	Ground
17	DCKN	Negative LVDS differential clock inputs
18	DCKP	Positive LVDS differential clock inputs
19	GND	Ground
20	D3N	Negative LVDS differential data inputs
21	D3P	Positive LVDS differential data inputs
22	GND	Ground
23-24	NC	Disconnect pin
25	GND	Ground
26	NC	Disconnect pin
27	DIMO	Backlight dimmer signal for external controller. DIMO=H, Logical control signal to turn on external backlight controller. DIMO=L, Turn off external backlight controller.

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Pin NO.	Symbol	Description
28	HSD	6-bit/8-bit input select. HSD= "L", 8-bit; HSD= "H", 6-bit
29	AVDD	Power supply for analog circuits
30	GND	Ground
31,32	LED_K	LED Cathode
33	L/R	Source Right or Left sequence control. Normally pull high. SHLR = "L", shift left; SHLR = "H", shift right
34	U/D	Gate Up or Down scan control. Normally pull low. UPDN = "L", From top to bottom; UPDN = "H", From bottom to top.
35	VGL	Power supply for drive output Low
36	CABCEN1	CABC H/W enable pin. Normally pull low. When CABC_EN=" 00", CABC OFF. (Default mode) When CABC_EN=" 01", User interface Image.
37	CABCEN0	When CABC_EN=" 10", Still Picture. When CABC_EN=" 11", Moving Image.
38	VGH	Power supply for drive output High
39,40	LED_A	LED Anode
- END -		

3.2 Wiring Diagram



4. Electrical Characteristics

4-1 TFT LCD Module Operating Conditions

Item	Symbol	Condition	Min	Type	Max	Unit
Analog Power supply	VCI	-	3.0	3.3	3.6	V
Power For Analog Circuit	AVDD	-	10.2	10.4	10.6	V
TFT Gate on voltage	VGH	VGH-VGL	22.0	23.0	24.0	V
TFT Gate off voltage	VGL	$\leq 40V$	-8.8	-7.8	-6.8	V
TFT Common Electrode Voltage	VCOM	-	3.2	3.6	4.0	V

Note: TFT VCOM PIN will generate large current. In order to keep the TFT VCOM output and TFT display stable, it is recommended to use VCOM BUFFER driver chip

4-2 LED back light specification (per chip)

Item	Symbol	Condition	Min	Type	Max	Unit
Forward voltage	Vt	If=20mA	9.0	9.6	10.2	V
Forward current	Ipn	/1-chip	-	180	-	mA
Luminance(With LCD)	Lv	If=180mA	-	300	-	cd/m ²
Luminous color	White					

5. OPTICAL SPECIFICATION

The test of Optical specifications shall be measured in a dark room (ambient luminance 1lux and temperature = 25 ± 2°C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. The center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement.

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle Range	Horizontal	Θ L	CR>10	-	75	-	Deg.	Note 1
		Θ R		-	75	-	Deg.	
	Vertical	Θ U		-	70	-	Deg.	
		Θ D		-	75	-	Deg.	
Contrast ratio		CR	$\Theta = 0^\circ$	-	500	-		Note2
Color Gamutt (C light)		CG		-	50	-	%	
White Chromaticity		Wx	$\Theta = 0^\circ$	-0.03	0.293	+0.03		Note4 (Based on C Light)
		Wy			0.323			
Reproduction of color	Red	Rx			0.616			
		Ry			0.335			
	Green	Gx			0.284			
		Gy			0.538			
	Blue	Bx			0.148			
		By			0.141			
Response Time (Rising + Falling)		Tr+Tf	$\Theta = 0^\circ$ Ta= 25°C	-	20	30	ms	Note5
Transmittance		Tr		-	4.9		%	Note3

Note :

1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o' clock direction and the vertical or 6, 12 o' clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 5).

2. Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state . (see FIGURE 5) Luminance Contrast Ratio (CR) is defined mathematically.

CR =Luminance when displaying a white raster / Luminance when displaying a black raster

3. Transmittance is the Value with Polarizer.

4. The color chromaticity coordinates specified in the above table shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

5. The electro-optical response time measurements shall be made as FIGURE 6 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_d .

Figure 4. The Definition of V_{th} & V_{sat}

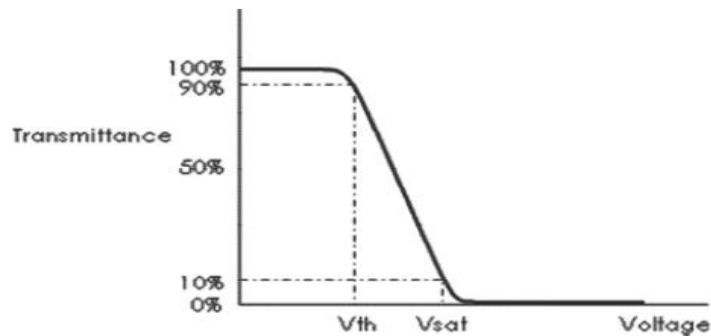


Figure 5. Measurement Set Up

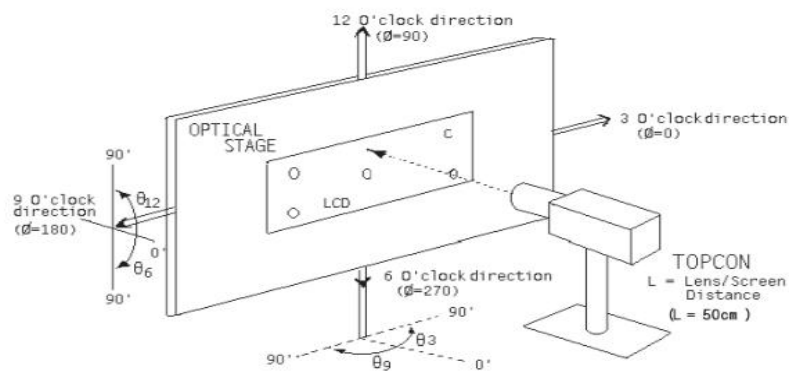
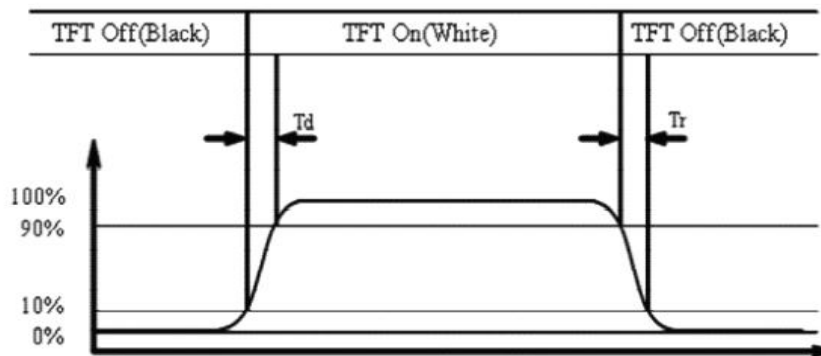


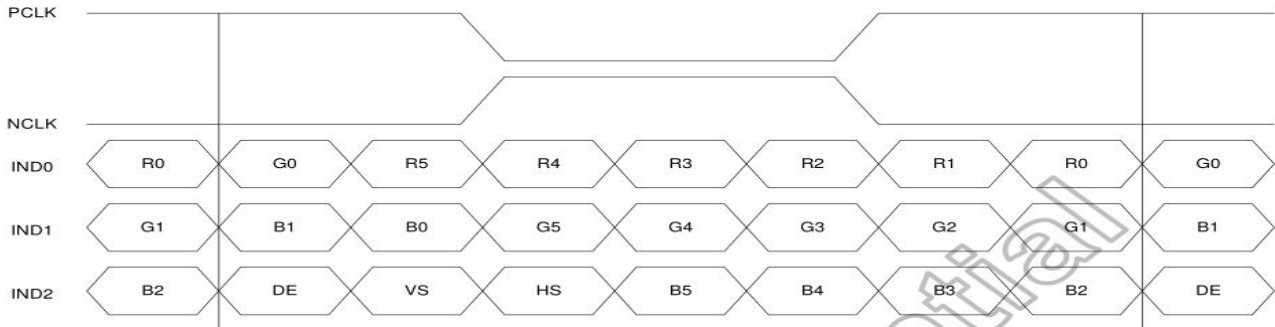
Figure 6. Response Time Testing



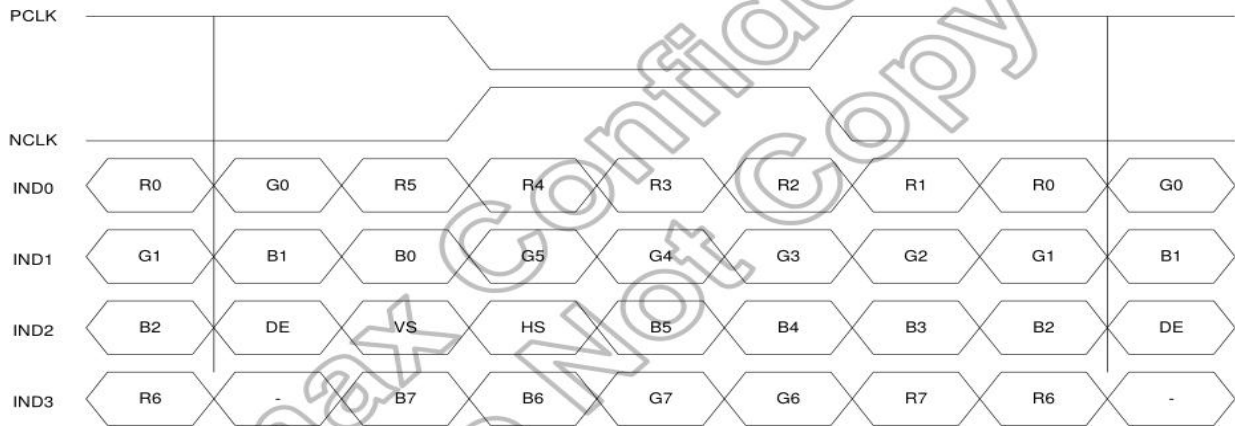
6. Timing Characteristics of Input Signals

6-1 Data Input Format

6-1-1 6-bits LVDS Input



6-1-2 8-bits LVDS Input



6-1 Timing table

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
DCLK Frequency	fclk	57	65	70.5	MHz
Horizontal Display Area	thd	1024			DCLK
HS Period	th	1200	1344	1400	DCLK
HS Pulse Width	thpw	1	-	140	DCLK
HS Back Porch	thbp	160			DCLK
HS Front Porch	thfp	16	160	216	DCLK

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd	768			T_H
VS Period	tv	792	806	840	T_H
VS Pulse Width	tvpw	1	-	20	T_H
VS Back Porch	tvbp	23			T_H
VS Front Porch	tvfp	1	15	49	T_H